

About the University

Sandip University is a thriving hub of 21st-century higher education. It is a UGC-approved University in India, located in Nashik, Maharashtra. The University is set in a picturesque lush green Wi-Fi-enabled campus spanning across 250+ acres and is home to cutting-edge infrastructure for a holistic student experience. The University offers programs in Engineering and Technology, Computer Science and Engineering, Commerce and Management Studies, Pharmaceutical Sciences, Law, Sciences, Design, and Vocational Studies.

Sandip University's educational programs are designed to be in sync with **Education 4.0** and **Industry 4.0** while paving the way for **Education 5.0** and **Industry 5.0**. Sandip University prepares students of today to become leaders of tomorrow on the global stage. Sandip University aims to reimagine the standards for higher education in India and around the world.

About the Department

The Department of Electrical and Electronics Engineering at the School of Engineering and Technology, Sandip University has a top-level quality assessment rating for teaching and a reputation for research among the Engineering colleges in Nashik. The department was established after the inception of Sandip University with the avowed intent and creating highly qualified engineers and researchers who can make a substantial contribution to the field of Electrical and Electronics Engineering. At present, the department is conducting a UG program in Electrical and Electronics Engineering, Electrical Engineering, Electronics Engineering, Electrical Engineering, Electronics and Telecommunication Engineering, PG programs (Electrical Power System, and Electric Vehicles), and a Ph.D. program.

Organizing Committee

Chief Patron

Dr. Sandip Kumar Jha

Chairman, Sandip Group of Institutions

Patrons

Dr. Rajendra Sinha

Vice Chancellor, Sandip University, Nashik

Prof. Pramod Karole

OSD, Sandip University, Nashik

Mr. Vivek Nikam

OSD, Sandip University, Nashik

Dr. Prasad Baviskar

Registrar, Sandip University, Nashik

Convener

Dr. Prakash G. Burade, Associate Dean, SOET

Co-Convener

Dr. Mangesh Nikose, HoD, EEE

Coordinator of the Programme

Dr. Jitesh Shinde

Ms. Shraddha Zanjat

Organizer Team

Mr. Harshal Chaughule

Mr. Harshal Shelar

REGISTRATION ENQUIRY

Dr. Jitesh Shinde

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One Week (Hybrid Mode) Faculty Development Programme On

Innovations & Recent Advancements in Electrical and Electronics Engineering

15th January to 19th January 2024



Organized by

DEPARTMENT OF ELECTRICAL AND
ELECTRONICS ENGINEERING

SCHOOL OF ENGINEERING &
TECHNOLOGY

Approved by UGC, New Delhi,

Sandip University, Mahiravani, Tal & Dist: Nashik
Maharashtra, India, 422213

www.sandipuniversity.edu.in

ABOUT THE PROGRAMME

In the ever-changing world of software technologies, it is extremely important to keep abreast with current technologies and innovations.

This FDP aims to provide a fraternity of intellects with a comprehensive overview of the latest trends, technologies, and research developments in the field of Electrical and Electronics Engineering (EEE). This program will typically cover a wide range of topics, including:

- Emerging technologies in Electrical Engineering: This may include topics such as control systems, power electronics, and renewable energy
- Recent advancements in Electronics Engineering: This may include topics such as communication systems, IoTs, Artificial Intelligence, Machine Learning, and microelectronics
- Innovation in EEE education: This may include outcome-based education (OBE), blended learning approach, and NEP2020 policy

This FDP has been planned to bridge the gap between engineering education at the academic and industry levels.

OBJECTIVES

The objectives of the FDP are to

- Enhance the knowledge and skills of faculty members in the latest trends and advancements in electrical and electronics engineering
- Improve the teaching and learning methodologies of faculty members
- Promote research and innovation in electrical and electronics engineering
- Enhance the overall quality of education in EEE
- Promote collaborative research

TARGET PARTICIPANTS

The FDP is open to faculty members from AICTE-approved engineering colleges, Polytechnic institutes, candidates working in industrial / research organizations, and research scholars having an electrical and electronics engineering background.

RESOURCE PERSONS

Experts from academia, industries, and research labs of national and international importance.

REGISTRATION DETAILS

Last date for registration: **12/01/2024**

Google form Registration link:

<https://forms.gle/SUfeKXaDjJ1S6KQWA>

Registration fees:

Research scholars: 150/- INR

Faculty & Industry Person: 200/- INR

REGISTRATION PAYMENT

Steps to follow for Online transactions:

(G-Pay/ Paytm/ Phone pay / UPI)

1. Payment of Registration fee:

Phone Pay No: 9960290768

UPI-ID: shraddhazanjat@ybl

or

For NEFT

Account Name: SHRADDHA ZANJAT

Account Number: **3714841624**

Name of the Bank: Central Bank of India

IFSC code: CBIN0284422

2. Take a Screenshot of the Transaction of fee payment for uploading
3. Fill out the registration form via the Google form link
4. E-certificates will be provided upon confirmation of registration fee payment and having an attendance of at least 80 percent

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